



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: NOGUCHI et al.
Serial No.: 09/825,946
Filed: April 5, 2001
For: FABRICATION METHOD FOR SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE [as amended]
Group: 2811
Examiner: H. Vu

AMENDMENT

MS Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 10, 2003

Sir:

In response to the Office Action mailed August 14, 2003, please amend the above-identified application as set forth herein. This paper includes the following items:

Amendment to the Specification;
Amendment to the Claims; and
Remarks.

RECEIVED
DEC 15 2003
TECHNOLOGY CENTER 2800